

Position: SoC Verification Engineer

Location: FINLAND, Oulu

Nokia is a global leader in the technologies that connect people and things. With state-of-the-art software, hardware and services for any type of network, Nokia is uniquely positioned to help communication service providers, governments, and large enterprises deliver on the promise of 5G, the Cloud and the Internet of Things. Serving customers in over 100 countries, our research scientists and engineers continue to invent and accelerate new technologies that will increasingly transform the way people and things communicate and connect.

With the collaboration of EURES Portugal and Finland, Nokia Solutions and Networks Oy is looking for an engineer with a specialist level of experience in RTL verification activities.

Your role

As a member of SoC DU organization you will participate to RTL verification within your responsibility area. You will also have the responsibilities in specifying features and creating verification plans, developing test environments, creating test cases, running simulations, analyzing and debugging the design, reviewing documents, and creating verification documentation for a SoC/IP block area.

Main assignments

- Participate in outlining System-on-Chip or IP verification activities
- Preparation and review of related verification documents and review of functional and design specification for SoC/IP
- RTL verification environment and VIP development
- Test case development, running regressions and debugging
- Prepares and reviews System-on-Chip development documentations and application documents
- Cooperate with system engineers, HW/SW development, suppliers and other relevant functions to solve technical issues for quality.
- Supporting other RTL verification, integration and prototyping teams
- Support HW/SW Bring-up and debug

Required profile

- Master Degree in Engineering or equivalent
- Over 6 years working experience in RTL verification (junior specialists also welcome to apply)
- Good understanding of digital signal processing and related mathematics
- Specialist with SystemVerilog and UVM usage, many years of coding and verification experience
- Understanding VHDL/Verilog language and design debugging methods
- Very good knowledge of SoC (ASIC/FPGA/low level SW) design and verification tools.
- Basic understanding of Cellular networks and 2G, 3G, LTE and 5G technology
- Fluent spoken and written English

The company offers

- Contract duration more than 12 months (permanent)
- Salary according to agreement
- For senior consultants a relocation package is possible

How to apply: <https://aluperf.referrals.selectminds.com/jobs/soc-verification-engineer-60598>

Further info: on Nokia & its careers: <https://www.nokia.com/> | <https://careers.nokia.com/>

Please contact and give knowledge of your application
to EURES Portugal at the attention of Nídia Figueiredo @ euresPToutgoing@iefp.pt

EURES mobility supports may be available if you apply to this vacancy. Please check www.iefp.pt/eures