

Position: Implementation Engineer, DFE ASIC

Location: FINLAND, Oulu

Nokia is a global leader in the technologies that connect people and things. With state-of-the-art software, hardware and services for any type of network, Nokia is uniquely positioned to help communication service providers, governments, and large enterprises deliver on the promise of 5G, the Cloud and the Internet of Things. Serving customers in over 100 countries, our research scientists and engineers continue to invent and accelerate new technologies that will increasingly transform the way people and things communicate and connect.

With the collaboration of EURES Portugal and Finland, Nokia Solutions and Networks Oy is looking for SoC Implementation (Synthesis & STA) Engineer to SoC DU organization.

Your role

Basic requirements for implementation specialist are very good understanding and practical experience of synthesis, STA, timing closure and formal verification. The precise job description of the main tasks can be tailored based on your interest and experience.

Main assignments

- Participate in outlining System-on-Chip top-level activities
- Work with various design groups across different disciplines (RTL design, verification, DFT & layout) to meet timing closure, area, power and performance requirements
- Perform logic synthesis, logic equivalence checking, static timing analysis, power estimation and hand-off checks using best-in-class methodologies
- Analyze log and report files to ensure the tools are getting the required results
- Develop the scripts to get the required results within the scheduled milestones
- Advising RTL designers and junior implementation designers related to constraints, timing closure & vendor requirements

Required profile

- Bachelor's or Master Degree in Engineering or equivalent
- 6+ years working experience in FPGA/ASIC/SoC development (but junior specialists are also welcome to apply)
- Familiar with digital design and UNIX/LINUX environment
- Knowledge of ARM standards, processors and interconnects
- Good understanding of RTL design and RTL requirements for synthesis
- Good experience of scripting languages like TCL, Python, Perl
- Good knowledge of SoC (ASIC/FPGA) implementation tools
- Understanding of Cellular networks and 2G, 3G, LTE and 5G technology
- Fluent spoken and written English

Beneficial skills

- Low power understanding
- Experience of DFT, P&R or floorplanning
- Knowhow of packages, thermal, testing, product engineering

The company offers

- Contract duration more than 12 months (permanent)
- Salary according to agreement
- For senior consultants a relocation package is possible

Apply: <https://aluperf.referrals.selectminds.com/jobs/implementation-engineer-dfe-asic-61817>

Further info: on Nokia & its careers: <https://www.nokia.com/> | <https://careers.nokia.com/>

Please contact and give knowledge of your application
to EURES Portugal at the attention of Nídia Figueiredo @ euresPToutgoing@iefp.pt

EURES mobility supports may be available if you apply to this vacancy. Please check www.iefp.pt/eures